

10783886

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Om P. Agrawal; Howard Tang; Jack Wong
Assignee: Lattice Semiconductor Corporation
Title: Upgradeable And Reconfigurable Programmable Logic Device
Serial No.: Unknown Filing Date: Herewith
Examiner: Unassigned Group Art Unit: Unknown
Docket No.: M-15311 US

Irvine, California
February 20, 2004

Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit three (3) sheets of formal drawings, consisting of Figures 1, 2, 3, and 4, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

Express Mail #EV411212055US

Respectfully submitted,



Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940

LAW OFFICES OF
PETERSON KWOK CHEN
& YEED LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049